



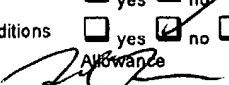
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APPLICANTS Scott T. Becker, San Jose, CA; <p style="text-align: right;">A/0 w US 6,410,304 8/26/02 JSC</p>					
** CONTINUING DATA THIS APPLICATION IS A CON OF 09/442,877 11/18/1999 WHICH IS A DIV OF 09/015,427 01/29/1998 PAT 6,016,390 <p style="text-align: right;">JSC</p>					
** FOREIGN APPLICATIONS					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 01/26/2002					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Verified and Acknowledged  Examiner's Signature 		STATE OR COUNTRY CA	SHEETS DRAWING 10	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 2
ADDRESS Albert S. Penilla Martine & Penilla, LLP 710 Lakeway Drive, Suite 170 Sunnyvale, CA 94087					
TITLE Methods for reducing bitline voltage offsets in memory devices					
FILING FEE RECEIVED 740	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other <input type="checkbox"/> Credit			